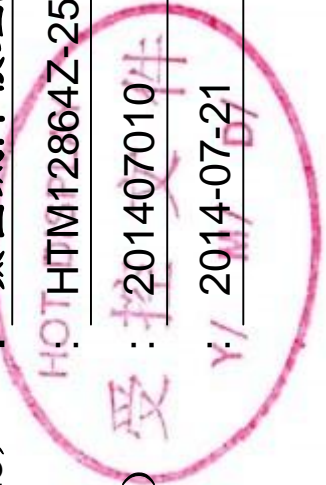




# HTM12864Z-25W-A5PP

产品名称 (Product name) : 黑白点阵模组(简体字库)  
 型号 (Model) : HOT HTM12864Z-25W-A5P  
 编号 (Part number) : 201407010  
 日期 (Date) : 2014-07-21



<b>深圳市鑫洪泰电子科技有限公司</b> Shenzhen Hot Display Technology Co.,Ltd		
编制 Prepared by	审核 Checked by	核准 Approved by

编码: QR-R-011 A/0

序号:

Rev.	Descriptions	Date
01	Preliminary Release	2014-07-21



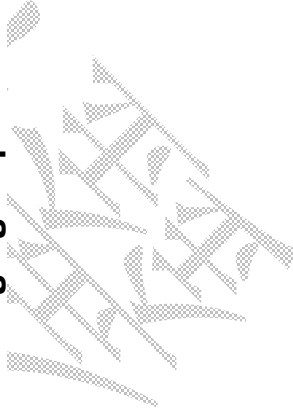
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HTM12864Z-25W-A5P

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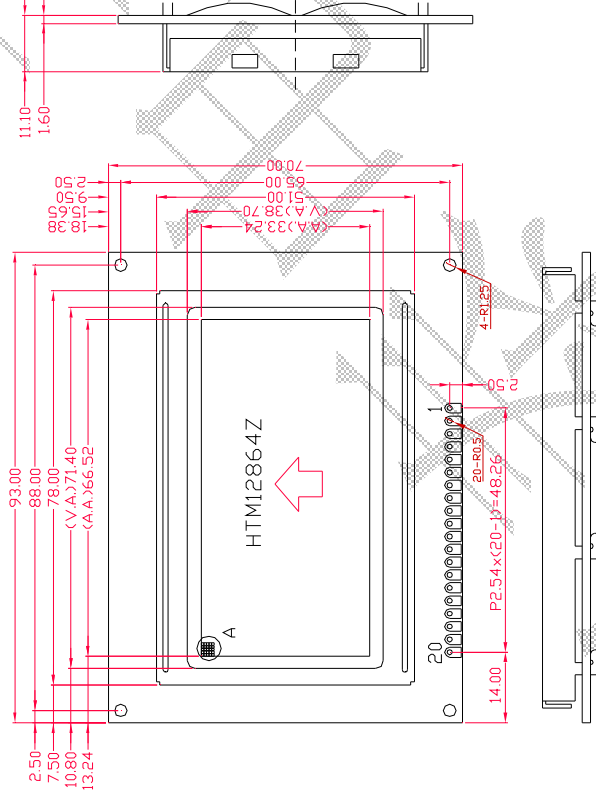
## 1. Basic Specifications

### 1.1 Display Specifications

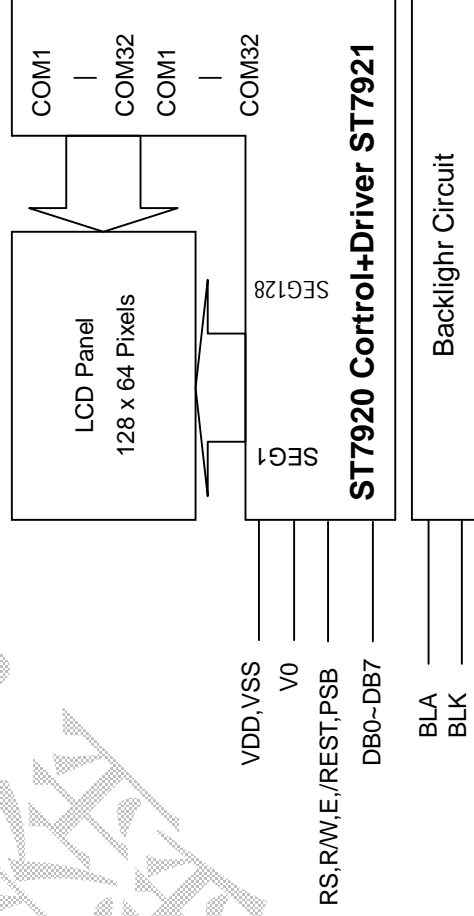
- 1>LCD Display Mode : STN, Negative, Blue, Transmissive
- 2>Viewing Angle : 6H
- 3>Driving Method : 1/33 Duty, 1/5 Bias
- 4 >Backlight : White LED

### 1.2 Mechanical Specifications

- 1>Outline Dimension : 93.0X70.0 X11.1mm (See attached Outline Drawing for Deta)



### 1.3 Circuit Diagram

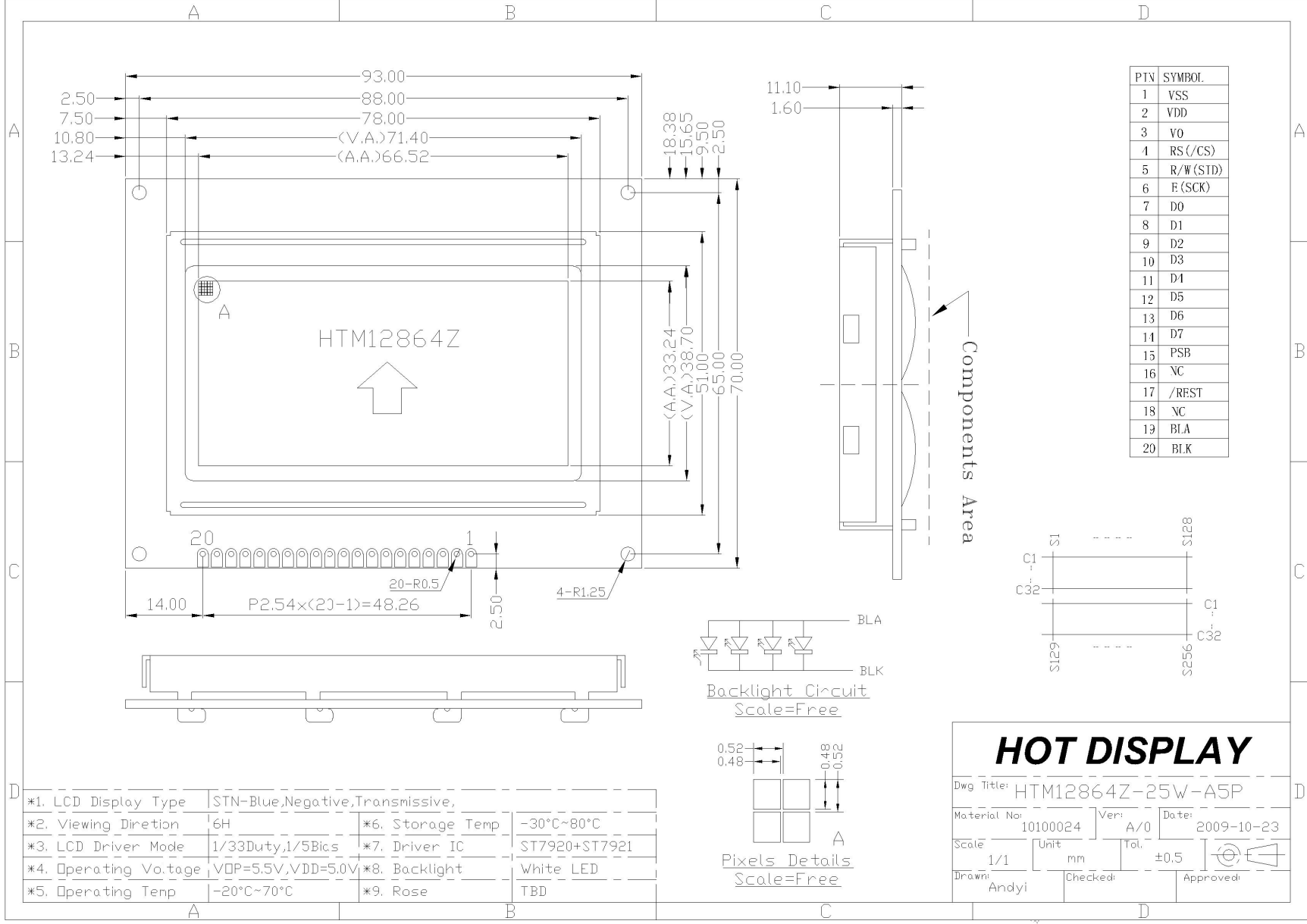


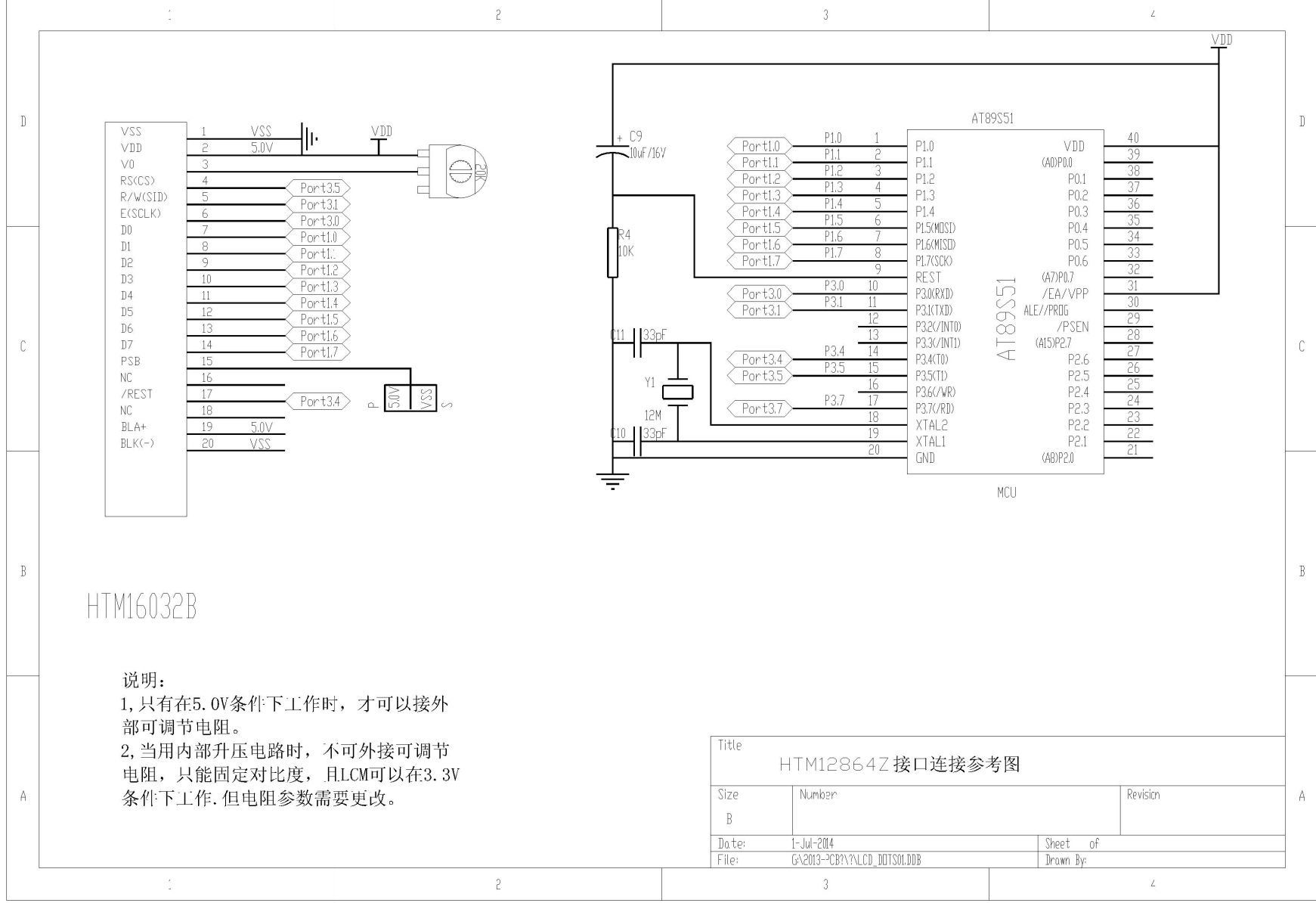


1.4 Terminal Function

Pin No.	Pin Name	Function
1	VSS	LCD Negtive(VSS)
2	VDD	LCD Positive(5.0V)
3	V0	LCD Contrast Adjust
4	RS(CS)	Parallel Mode: Register select. <b>0</b> : Select instruction register (write) or busy flag, address counter (read); <b>1</b> : Select data register (write/read). Serial mode: Chip select. <b>1</b> : chip enabled; <b>0</b> : chip disabled. When chip is disabled, SID and SCLK should be set as "H" or "L". Transcient of SID and SCLK is not allowed.
5	R/W(SID)	Parallel Mode: Read/Write control. <b>0</b> : Write; <b>1</b> : Read. Serial Mode: Sserial data input.
6	E(SCLK)	Parallel Mode: <b>1</b> : Enable trigger. Serial Mode: Serial clock.
7~14	D0~D7	Data Buss
15	PSB	Interface selection: <b>0</b> : serial mode; <b>1</b> : 8/4-bit parallel bus mode.
16	NC	
17	/REST	System reset input (low active).
18	NC	
19	BLA	Bcaklight Positive(5.0V)
20	BLK	Bcaklight Negative(VSS)

**1.5 Product Outline**



**1.6 Schematic Diagram**


## 2. Absolute Maximum Ratings

Items	Symbol	MIN.	MAX.	Unit	Condition
Supply Voltage	V <sub>DD</sub>	3.3	+5.5	V	V <sub>SS</sub> = 0V
	V <sub>CD</sub>	3.0	+7.0	V	V <sub>SS</sub> = 0V
Input Voltage	V <sub>IN</sub>	-0.3	V <sub>DD</sub> +0.3	V	V <sub>SS</sub> = 0V
Operating Temperature	T <sub>OP</sub>	-20	+70	°C	No Condensation
Storage Temperature	T <sub>st</sub>	-30	+80	°C	No Condensation

## 3. Electrical Characteristics

3.11 DC Characteristics (TA= 25 °C, VDD=2.7V~ 4.5V)

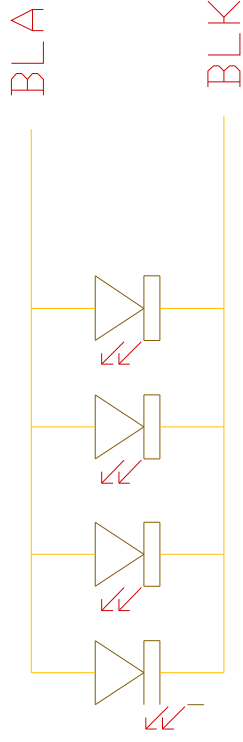
Symbol	Characteristics	Test Condition	Min.	Typ.	Max.	Unit
V <sub>DD</sub>	Operating Voltage	-	2.7	-	5.5	V
V <sub>LCD</sub>	LCD Voltage	V <sub>O</sub> -V <sub>SS</sub>	3.0	-	7	V
I <sub>CC</sub>	Power Supply Current	f <sub>osc</sub> = 530KHz, V <sub>DD</sub> =3.0V R <sub>F</sub> =18KΩ	-	0.20	0.45	mA
V <sub>IHI</sub>	Input High Voltage (Except OSC1)	-	0.7V <sub>DD</sub>	-	V <sub>DD</sub>	V
V <sub>IL1</sub>	Input Low Voltage (Except OSC1)	-	-0.3	-	0.6	V
V <sub>IH2</sub>	Input High Voltage (OSC1)	-	V <sub>DD</sub> -1	-	V <sub>DD</sub>	V
V <sub>IL2</sub>	Input Low Voltage (OSC1)	-	-	-	1.0	V
V <sub>OHI</sub>	Output High Voltage (DB0 - DB7)	I <sub>OH</sub> = -0.1mA	0.8V <sub>DD</sub>	-	V <sub>DD</sub>	V
V <sub>OL1</sub>	Output Low Voltage (DB0 - DB7)	I <sub>OL</sub> = 0.1mA	-	-	0.1	V
V <sub>OHI2</sub>	Output High Voltage (Except DB0 - DB7)	I <sub>OH</sub> = -0.04mA	0.8V <sub>DD</sub>	-	V <sub>DD</sub>	V
V <sub>OL2</sub>	Output Low Voltage (Except DB0 - DB7)	I <sub>OL</sub> = 0.04mA	-	-	0.1V <sub>DD</sub>	V
I <sub>LEAK</sub>	Input Leakage Current	V <sub>IN</sub> = 0V to V <sub>DD</sub>	-1	-	1	μA
I <sub>PUP</sub>	Pull Up MOS Current	V <sub>DD</sub> = 3V	22	27	32	μA

## 3.12 DC Characteristics (TA= 25 °C, VDD=4.5V)

Symbol	Characteristics	Test Condition	Min.	Typ.	Max.	Unit
V <sub>DD</sub>	Operating Voltage	-	4.5	-	5.5	V
V <sub>LCD</sub>	LCD Voltage	V <sub>0</sub> -V <sub>SS</sub>	3.0	-	7	V
I <sub>CC</sub>	Power Supply Current	f <sub>osc</sub> = 540KHz, V <sub>DD</sub> =5V R <sub>F</sub> =33KΩ	-	0.45	0.75	mA
V <sub>IHI</sub>	Input High Voltage (Except OSC1)	-	0.7V <sub>DD</sub>	-	V <sub>DD</sub>	V
V <sub>ILI</sub>	Input Low Voltage (Except OSC1)	-	-0.3	-	0.6	V
V <sub>IHI2</sub>	Input High Voltage (OSC1)	-	V <sub>DD</sub> -1	-	V <sub>DD</sub>	V
V <sub>ILI2</sub>	Input Low Voltage (OSC1)	-	-	-	1.0	V
V <sub>OHI</sub>	Output High Voltage (DB0 - DB7)	I <sub>OH</sub> = -0.1mA	0.8V <sub>DD</sub>	-	V <sub>DD</sub>	V
V <sub>OIL</sub>	Output Low Voltage (DB0 - DB7)	I <sub>OL</sub> = 0.1mA	-	-	0.4	V
V <sub>OHI2</sub>	Output High Voltage (Except DB0 - DB7)	I <sub>OH</sub> = -0.04mA	0.8V <sub>DD</sub>	-	V <sub>DD</sub>	V
V <sub>OIL2</sub>	Output Low Voltage (Except DB0 - DB7)	I <sub>OL</sub> = 0.04mA	-	-	0.1V <sub>DD</sub>	V
I <sub>LEAK</sub>	Input Leakage Current	V <sub>IN</sub> = 0V to V <sub>DD</sub>	-1	-	1	μA
I <sub>PUP</sub>	Pull Up MOS Current	V <sub>DD</sub> = 5V	75	80	85	μA

## 3.2 LED Backlight Circuit

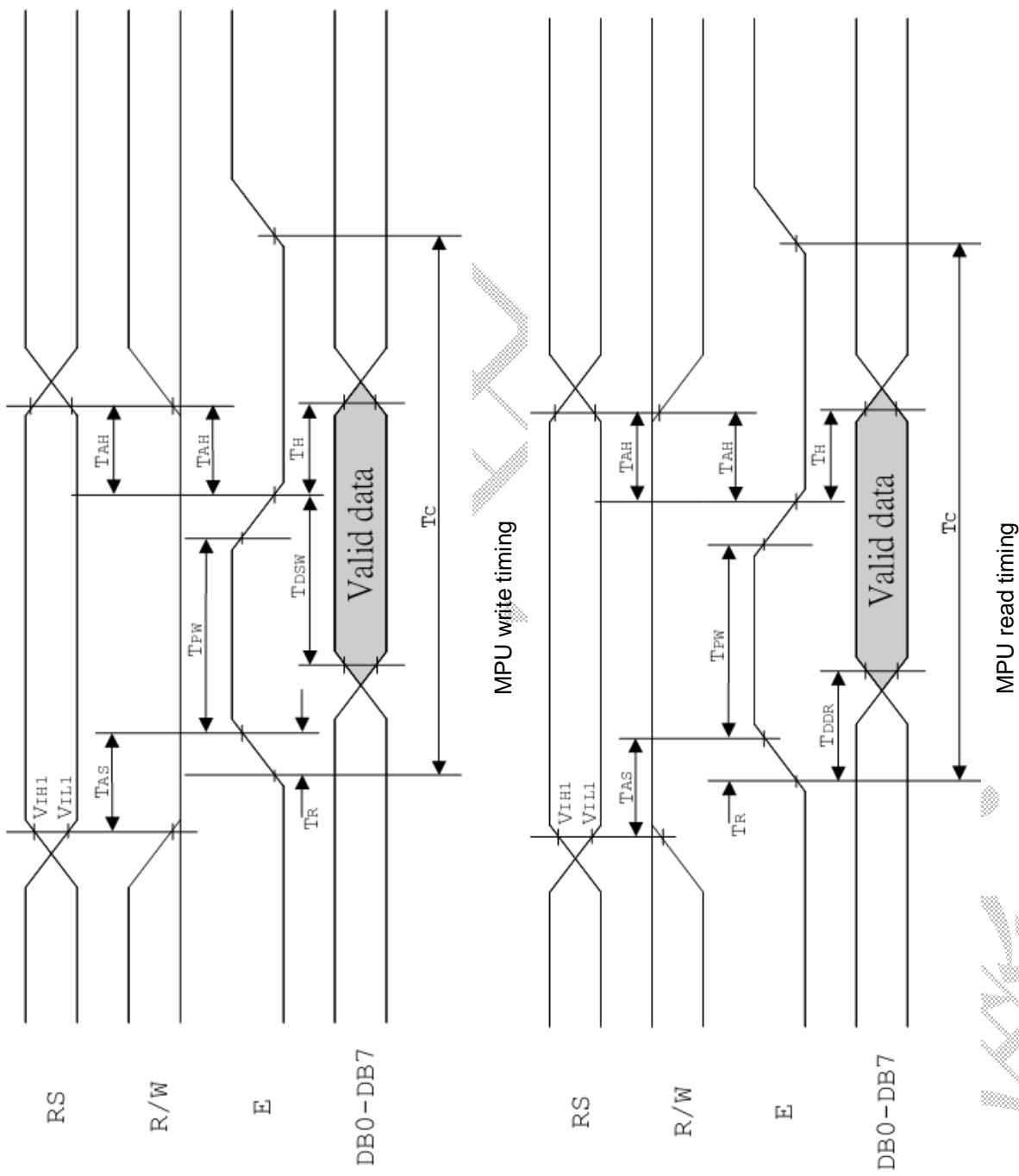
Items	Symbol	MIN.	TYP.	MAX.	Unit	Condition
Forward Voltage	V <sub>f</sub> BLA	-	3.1	-	V	Without Resistance
Forward Current	I <sub>f</sub> BLA	-	60	80	mA	-





### 3.3 AC Characteristics

#### 3.3.1 Parallel Mode Interface



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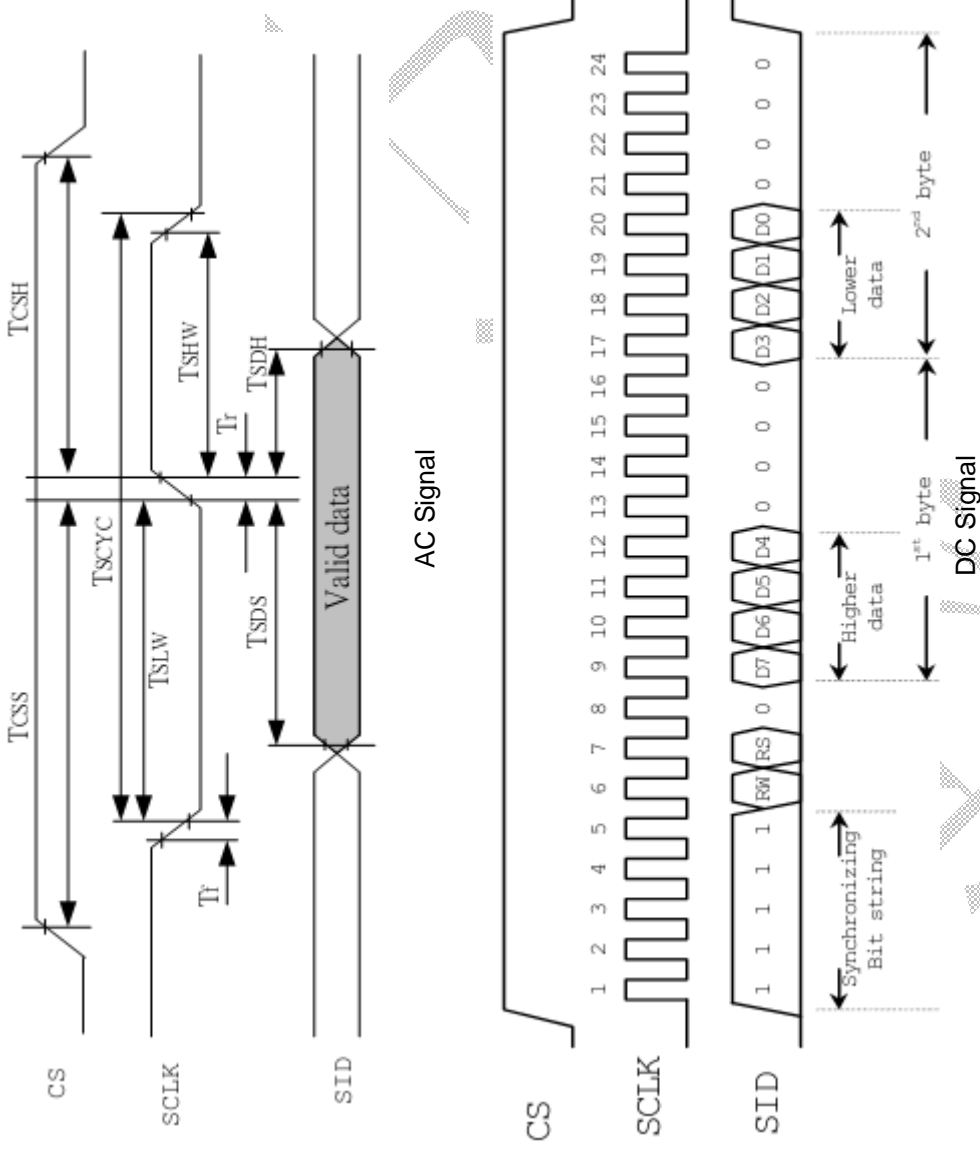
(TA= 25 °C, VDD= 4.5V)

Symbol	Characteristics	Test Condition	Min.	Typ.	Max.	Unit
<i>Internal Clock Operation</i>						
f <sub>osc</sub>	OSC Frequency	R = 33KΩ	480	540	600	KHz
<i>External Clock Operation</i>						
f <sub>EX</sub>	External Frequency	-	480	540	600	KHz
	Duty Cycle	-	45	50	55	%
T <sub>R</sub> ,T <sub>F</sub>	Rise/Fall Time	-	-	-	0.2	μs
<i>Write Mode (Writing data from MPU to ST7920)</i>						
T <sub>C</sub>	Enable Cycle Time	Pin E	1200	-	-	ns
T <sub>PW</sub>	Enable Pulse Width	Pin E	140	-	-	ns
T <sub>R</sub> ,T <sub>F</sub>	Enable Rise/Fall Time	Pin E	-	-	2.5	ns
T <sub>AS</sub>	Address Setup Time	Pins: RS,RW,E	10	-	-	ns
T <sub>AH</sub>	Address Hold Time	Pins: RS,RW,E	20	-	-	ns
T <sub>DSW</sub>	Data Setup Time	Pins: DB0 - DB7	40	-	-	ns
T <sub>H</sub>	Data Hold Time	Pins: DB0 - DB7	20	-	-	ns
<i>Read Mode (Reading Data from ST7920 to MPU)</i>						
T <sub>C</sub>	Enable Cycle Time	Pin E	1200	-	-	ns
T <sub>PW</sub>	Enable Pulse Width	Pin E	140	-	-	ns
T <sub>R</sub> ,T <sub>F</sub>	Enable Rise/Fall Time	Pin E	-	-	2.5	ns
T <sub>AS</sub>	Address Setup Time	Pins: RS,RW,E	10	-	-	ns
T <sub>AH</sub>	Address Hold Time	Pins: RS,RW,E	20	-	-	ns
T <sub>DDR</sub>	Data Delay Time	Pins: DB0 - DB7	-	-	100	ns
T <sub>H</sub>	Data Hold Time	Pins: DB0 - DB7	20	-	-	ns
<i>Interface Mode with LCD Driver(ST7921)</i>						
T <sub>CWH</sub>	Clock Pulse with High	Pins: CL1, CL2	800	-	-	ns
T <sub>CWL</sub>	Clock Pulse with Low	Pins: CL1, CL2	800	-	-	ns
T <sub>CST</sub>	Clock Setup Time	Pins: CL1, CL2	500	-	-	ns
T <sub>SU</sub>	Data Setup Time	Pin: D	300	-	-	ns
T <sub>DH</sub>	Data Hold Time	Pin: D	300	-	-	ns
T <sub>DM</sub>	M Delay Time	Pin: M	-1000	-	1000	ns

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(TA= 25 °C, VDD= 2.7V)

Symbol	Characteristics	Test Condition	Min.	Typ.	Max.	Unit
<i>Internal Clock Operation</i>						
f <sub>OSC</sub>	OSC Frequency	R = 18KΩ	470	530	590	KHz
<i>External Clock Operation</i>						
f <sub>EX</sub>	External Frequency	-	470	530	590	KHz
	Duty Cycle	-	45	50	55	%
T <sub>R,T F</sub>	Rise/Fall Time	-	-	-	0.2	μs
<i>Write Mode (Writing data from MPU to ST7920)</i>						
T <sub>C</sub>	Enable Cycle Time	Pin E	1800	-	-	ns
T <sub>PW</sub>	Enable Pulse Width	Pin E	160	-	-	ns
T <sub>R,T F</sub>	Enable Rise/Fall Time	Pin E	-	-	25	ns
T <sub>AS</sub>	Address Setup Time	Pins: RS,RW,E	10	-	-	ns
T <sub>AH</sub>	Address Hold Time	Pins: RS,RW,E	20	-	-	ns
T <sub>DSW</sub>	Data Setup Time	Pins: DB0 - DB7	40	-	-	ns
T <sub>HI</sub>	Data Hold Time	Pins: DB0 - DB7	20	-	-	ns
<i>Read Mode (Reading Data from ST7920 to MPU)</i>						
T <sub>C</sub>	Enable Cycle Time	Pin E	1800	-	-	ns
T <sub>PW</sub>	Enable Pulse Width	Pin E	320	-	-	ns
T <sub>R,T F</sub>	Enable Rise/Fall Time	Pin E	-	-	25	ns
T <sub>AS</sub>	Address Setup Time	Pins: RS,RW,E	10	-	-	ns
T <sub>AH</sub>	Address Hold Time	Pins: RS,RW,E	20	-	-	ns
T <sub>DDR</sub>	Data Delay Time	Pins: DB0 - DB7	-	-	260	ns
T <sub>HI</sub>	Data Hold Time	Pins: DB0 - DB7	20	-	-	ns
<i>Interface Mode with LCD Driver(ST7921)</i>						
T <sub>CWH</sub>	Clock Pulse with High	Pins: CL1, CL2	800	-	-	ns
T <sub>CWL</sub>	Clock Pulse with Low	Pins: CL1, CL2	800	-	-	ns
T <sub>CST</sub>	Clock Setup Time	Pins: CL1, CL2	500	-	-	ns
T <sub>SU</sub>	Data Setup Time	Pin: D	300	-	-	ns
T <sub>DH</sub>	Data Hold Time	Pin: D	300	-	-	ns
T <sub>DM</sub>	M Delay Time	Pin: M	-1000	-	1000	ns

**3.32 Serial Mode Interface**


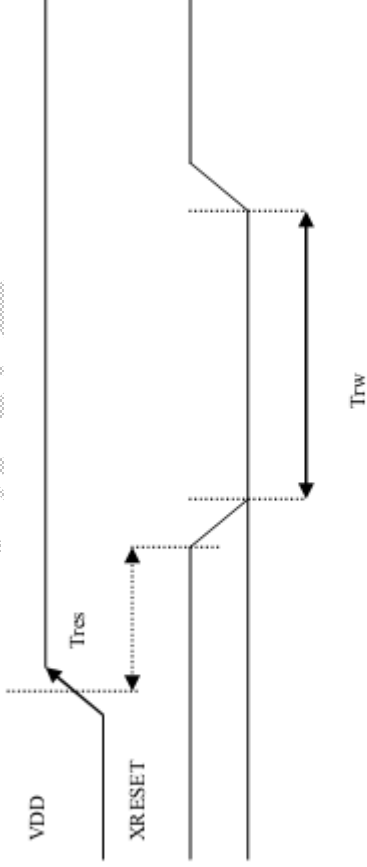
(TA= 25 °C, VDD= 4.5V)

Symbol	Characteristics	Test Condition	Min.	Typ.	Max.	Unit
<i>Internal Clock Operation</i>						
f <sub>osc</sub>	OSC Frequency	R = 33KΩ	470	530	590	KHz
<i>External Clock Operation</i>						
f <sub>EX</sub>	External Frequency	-	470	530	590	KHz
	Duty Cycle	-	45	50	55	%
T <sub>R</sub> , T <sub>F</sub>	Rise/Fall Time	-	-	-	0.2	μs
TSCYC	Serial clock cycle	Pin E	400	-	-	ns
TSHW	SCLK high pulse width	Pin E	200	-	-	ns
TSLW	SCLK low pulse width	Pin E	200	-	-	ns
TSDS	SID data setup time	Pins RW	40	-	-	ns
TSDH	SID data hold time	Pins RW	40	-	-	ns
TCSS	CS setup time	Pins RS	60	-	-	ns
TCSSH	CS hold time	Pins RS	60	-	-	ns

(TA= 25 °C, VDD= 2.7V)

Symbol	Characteristics	Test Condition	Min.	Typ.	Max.	Unit
<i>Internal Clock Operation</i>						
$f_{osc}$	OSC Frequency	R = 18K $\Omega$	470	530	590	KHz
<i>External Clock Operation</i>						
$f_{EX}$	External Frequency	-	470	530	590	KHz
	Duty Cycle	-	45	50	55	%
$T_{R,Tf}$	Rise/Fall Time	-	-	-	0.2	$\mu$ s
TSCYC	Serial clock cycle	Pin E	600	-	-	ns
TSHW	SCLK high pulse width	Pin E	300	-	-	ns
TSLW	SCLK low pulse width	Pin E	300	-	-	ns
TSDS	SID data setup time	Pins RW	40	-	-	ns
TSDH	SID data hold time	Pins RW	40	-	-	ns
TCSS	CS setup time	Pins RS	60	-	-	ns
TCSH	CS hold time	Pins RS	60	-	-	ns

### 3.4 Rest Timing



XRESET pulse width	Trw	10 $\mu$ s
RESET start time	Tres	50ns

## 4. Function specifications

### 4.1 The Parallel Interface

Shared	6800 Mode			Serial Mode			Function(6800)
	A0	R/W	E	RS(CS)	E(SCKL)	R/W(SID)	
H	H	H	H	-	-	-	Reads the display data
H	L	L	HàL	-	-	-	Writes the display data
L	H	H	H	-	-	-	Staus read
L	L	L	HàL	-	-	-	Write Command data

### 4.2 Display Memory Map

		COL					
PAGE	0x80/90	0x81/91			0x87/0x97		
	SEG0—SEG7	SEG8—SEG15	SEG16—SEG23	SEG24—SEG31	SEG112—SEG119	SEG120—SEG127	
ROW 0	D7--D0	D7--D0			D7--D0	D7--D0	
ROW 1	D7--D0	D7--D0			D7--D0	D7--D0	
ROW 30	D7--D0	D7--D0			D7--D0	D7--D0	
ROW 31	D7--D0	D7--D0			D7--D0	D7--D0	
	+						
	0x88/0x98			0x89/0x99			0x8F/0x9F
ROW0	D7--D0	D7--D0			D7--D0	D7--D0	
ROW 1	D7--D0	D7--D0			D7--D0	D7--D0	
ROW 30	D7--D0	D7--D0			D7--D0	D7--D0	
ROW 31	D7--D0	D7--D0			D7--D0	D7--D0	

#### Note:

- 1<sup>nd</sup> Page: from 0x80 ~ 0x87 (ROW0~ ROW15)
- 2<sup>nd</sup> Page: from 0x90~ 0x97 (ROW16~ ROW31)
- 3<sup>nd</sup> Page: from 0x88~ 0x8F (ROW32~ ROW47)
- 4<sup>nd</sup> Page: from 0x98~ 0x9F (ROW48~ ROW63)

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**4.3 Display Commands****Instruction Set 1: (RE=0: Basic Instruction)**

Inst.	Code																Description	Exec time (540KHZ)
	RS	RW	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0								
Display Clear	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	Fill DDRAM with "20H" and set DDRAM address counter (AC) to "00H".	1.6 ms
Return Home	0	0	0	0	0	0	0	0	0	0	0	0	1	X			Set DDRAM address counter (AC) to "00H", and put cursor to origin ; the content of DDRAM are not changed	72 us
Entry Mode Set	0	0	0	0	0	0	0	0	0	0	0	1	I/D	S			Set cursor position and display shift when doing write or read operation	72 us
Display Control	0	0	0	0	0	0	0	1	D	C	B						D=1: Display ON C=1: Cursor ON B=1: Character Blink ON	72 us
Cursor Display Control	0	0	0	0	0	1	S/C	R/L	X	X							Cursor position and display shift control; the content of DDRAM are not changed	72 us
Function Set	0	0	0	0	1	DL	X	0	RE	X	X						DL=1 8-bit interface DL=0 4-bit interface <b>RE=1: extended instruction</b> <b>RE=0: basic instruction</b>	72 us
Set CGRAM Address.	0	0	0	1	AC5	AC4	AC3	AC2	AC1	AC0							Set CGRAM address to address counter (AC) <b>Make sure that in extended instruction SR=0 (scroll or RAM address select)</b>	72 us
Set DDRAM Address.	0	0	1	0	AC5	AC4	AC3	AC2	AC1	AC0							Set DDRAM address to address counter (AC) AC6 is fixed to 0	72 us
Read Busy Flag (BF) & AC.	0	1	BF	AC6	AC5	AC4	AC3	AC2	AC1	AC0							Read busy flag (BF) for completion of internal operation, also Read out the value of address counter (AC)	0 us
Write RAM	1	0	D7	D6	D5	D4	D3	D2	D1	D0							Write data to internal RAM (DDRAM/CGRAM/GDRAM)	72 us
Read RAM	1	1	D7	D6	D5	D4	D3	D2	D1	D0							Read data from internal RAM (DDRAM/CGRAM/GDRAM)	72 us



**鑫洪泰****HOT DISPLAY 为您提供专业的显示解决方案****HTM12864Z-25W-A5P****Instruction set 2: (RE=1: extended instruction)**

Inst.	Code																Description	Exec time (540KHZ)
	RS	RW	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0								
Standby	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	Enter standby mode, any other instruction can terminate. COM1...32 are halted.	72 us	
Scroll or RAM Address. Select	0	0	0	0	0	0	0	0	0	0	0	0	1	SR	SR=1: enable vertical scroll position SR=0: enable CGRAM address ( <b>basic instruction</b> )	72 us		
Reverse (by line)	0	0	0	0	0	0	0	0	0	0	0	1	R1	R0	Select 1 out of 4 line (in DDRAM) and decide whether to reverse the display by toggling this instruction <b>R1,R0 initial value is 0,0</b>	72 us		
Extended Function Set	0	0	0	0	1	DL	X	RE	1	G	0					DL=1 :8-bit interface DL=0 :4-bit interface <b>RE=1: extended instruction set</b> <b>RE=0: basic instruction set</b> G=1 :graphic display ON G=0 :graphic display OFF	72 us	
Set Scroll Address	0	0	0	1	AC5	AC4	AC3	AC2	AC1	AC0						SR=1: AC5~AC0 the address of vertical scroll	72 us	
Set Graphic Display RAM Address	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	Set GDRAM address to address counter (AC) Set the vertical address first and followed the horizontal address by consecutive writings Vertical address range: AC5...AC0 Horizontal address range: AC3...AC0	72 us	

**Note:**

1. Make sure that ST7920 is not in busy state by reading the busy flag before sending instruction or data. If using delay loop instead, please make sure the delay time is enough. Please refer to the instruction execution time.
2. "RE" is the selection bit of basic and extended instruction set. After setting the RE bit, the value will be kept. So that the software doesn't have to set RE every time when using the same instruction set.



**鑫洪泰**

HOT DISPLAY 为您提供专业的显示解决方案

HTM12864Z-25W-A5P

**Initial Setting (Register flag) (RE=0: basic instruction)**

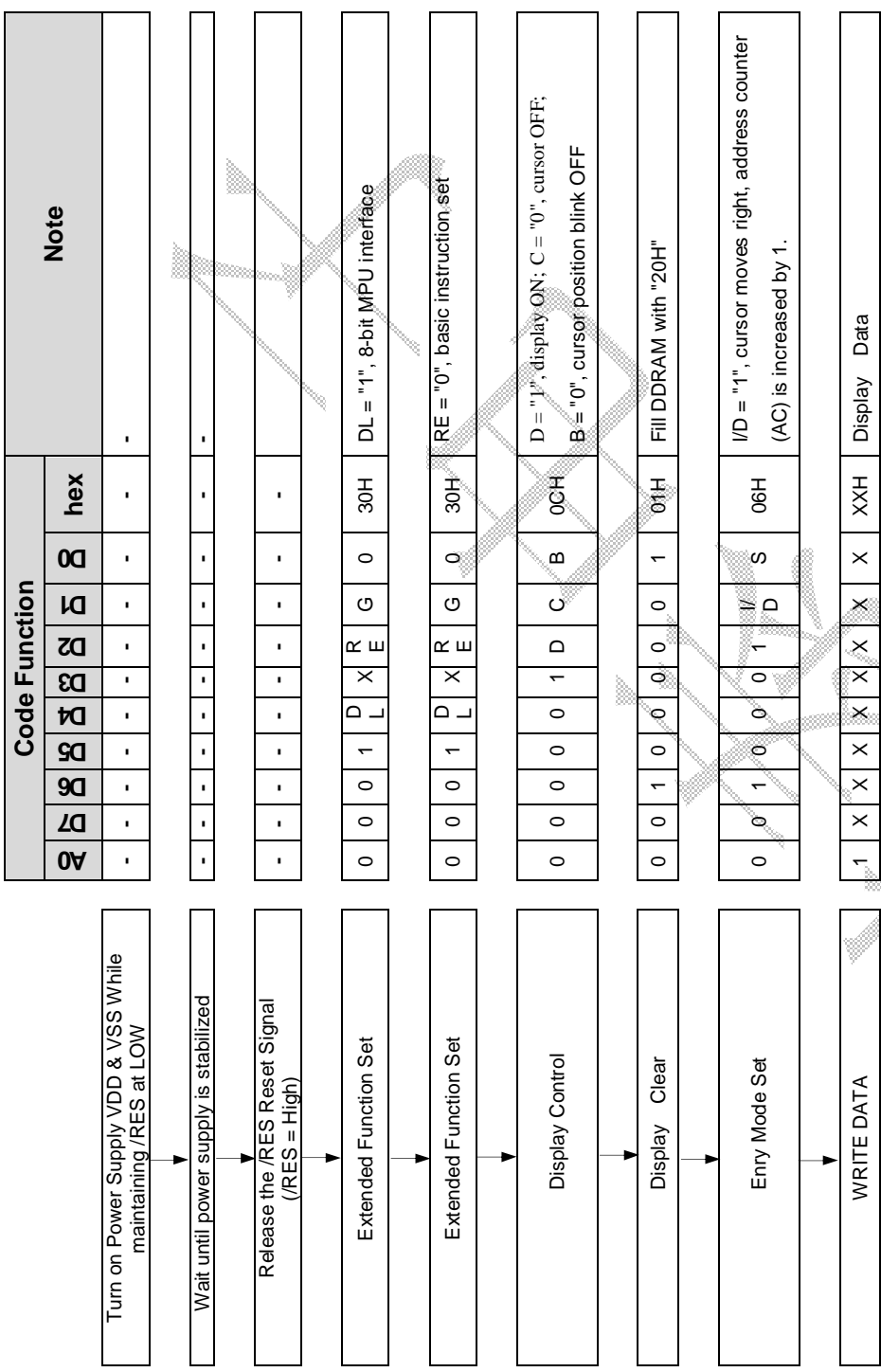
Inst.	Code										Description	
	RS	RW	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0		
Entry Mode Set	0	0	0	0	0	0	0	0	1	I/D	S	Cursor move to right , DDRAM address counter (AC) plus 1
										1	0	
Display Control	0	0	0	0	0	0	0	1	D	C	B	Display, cursor and blink are ALL OFF
									0	0	0	
CURSOR DISPLAY SHIFT	0	0	0	0	0	0	1	S/C	R/L	X	X	No cursor or display shift operation
								X	X			
FUNCTION SET	0	0	0	0	1	DL	X	RE	0	X	X	8-bit MPU interface , basic instruction set
						1			0			

**Initial Setting (Register flag) (RE=1: extended instruction set)**

Inst.	Code										Description	
	RS	RW	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0		
SCROLL OR RAM ADDR. SELECT	0	0	0	0	0	0	0	0	0	1	SR	Allow vertical scroll or set CGRAM address
											0	
REVERSE	0	0	0	0	0	0	0	0	1	R1	R0	Begin with normal and toggle to reverse
										0	0	
EXTENDED FUNCTION SET	0	0	0	0	1	DL	X	RE	1	G	0	Graphic display OFF
											0	

HOTLCD

#### 4.4 Basic Operating Sequence Initialization Sequence



#### WRITE DATA

#### Reference code

```

bit lcd_busy()
{
    bit result;
    rs = 0;
    rw = 1;
    ep = 1;
    _nop_();
    _nop_();
    _nop_();
    _nop_();
    result
    =(bit)((P1&0x80)>>7);
    ep = 0;
    return result;
}

void Comwrite
(Uchar com)
{

```

```

P1 = 0xFF;
while(lcd_busy());
rs = 0;
rw = 0;
_nop_();
_nop_();
P1 = com;
_nop_();
_nop_();
_nop_();
_nop_();
ep = 1;
_nop_();
_nop_();
_nop_();
_nop_();
ep = 0;
}

```

```

void Datwrite(Uchar dat)
{
    P1 = 0xFF;
    while(lcd_busy());
    rs = 1;
    rw = 0;
    P1 = dat;
    _nop_();
    _nop_();
    _nop_();
    _nop_();
    ep = 1;
    _nop_();
    _nop_();
    _nop_();
    _nop_();
    ep = 0;
}

```

```

void Setadd
(Uchar col,pag)
{
    Comwrite(0x36);
    Comwrite(pag+0x80);
    Comwrite(col+0x80);
    Comwrite(0x30);
}

void asc_tran
(Uchar *asc)
{
    while(*asc != 0)
    { Datwrite(*asc);
      asc++;
    }
}

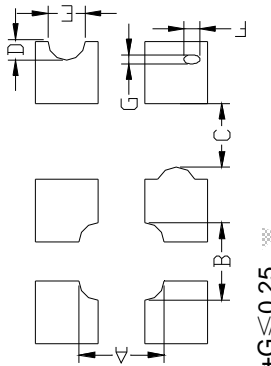
```

```

void hanzi_disp0()
{
    Comwrite(0x06);
    Comwrite(0x80);
    asc_tran("---HTM16032
Series--");
    Comwrite(0x90);
    asc_tran("**内含简体中
文字库**");
}

```

## 5. Inspection Standards

Item	Criterion for defects	Defect type								
1) Display on inspection	(1) Non display (3) Horizontal line is deficient (4) Cross line is deficient	Major								
2) Black / White spot	Size $\Phi$ (mm) $\Phi \leq 0.3$ Ignore (note) $0.3 < \Phi \leq 0.45$ 3 $0.45 < \Phi \leq 0.6$ 1 $0.6 < \Phi$ 0	Minor								
3) Black / White line	Length (mm) Width (mm) Acceptable number Ignore $L \leq 10$ $W \leq 0.03$ 3 $5.0 \leq L \leq 10$ $0.03 < W \leq 0.04$ 2 $5.0 \leq L \leq 10$ $0.04 < W \leq 0.05$ 2 $1.0 \leq L \leq 10$ $0.05 < W \leq 0.06$ 2 $1.0 \leq L \leq 10$ $0.06 < W \leq 0.08$ 1 $L \leq 10$ $0.08 < W$ follows 2) point defect Defects separate with each other at an interval of more than 20mm	Minor								
4) Display pattern	 <table border="1" style="margin-left: auto; margin-right: auto;"> <tr> <td><math>A+B \leq 0.28</math></td> <td><math>0 &lt; C</math></td> <td><math>D+E \leq 0.25</math></td> <td><math>F+G \leq 0.25</math></td> </tr> <tr> <td>2</td> <td>2</td> <td>2</td> <td>2</td> </tr> </table> Note: 1) Up to 3 damages acceptable 2) Not allowed if there are two or more pinholes every three-fourth inch.	$A+B \leq 0.28$	$0 < C$	$D+E \leq 0.25$	$F+G \leq 0.25$	2	2	2	2	Minor
$A+B \leq 0.28$	$0 < C$	$D+E \leq 0.25$	$F+G \leq 0.25$							
2	2	2	2							
5) Spot-like contrast irregularity	Size $\Phi$ (mm) Acceptable Number Ignore (note) $\Phi \leq 0.7$ 3 $0.7 < \Phi \leq 1.0$ 1 $1.0 < \Phi \leq 1.5$ 1 $1.5 < \Phi$ 0	Minor								
6) Bubbles in polarizer	Note: 1) Conformed to limit samples. 2) Intervals of defects are more than 30mm. Size $\Phi$ (mm) Acceptable Number Ignore (note) $\Phi \leq 0.4$ 2 $0.4 < \Phi \leq 0.65$ 1 $0.65 < \Phi \leq 1.2$ 1 $1.2 < \Phi$ 0	Minor								
7) Scratches and dent on the polarizer	Scratches and dent on the polarizer shall be in the accordance with "2) Black/white spot", and "3) Black/White line".	Minor								
8) Stains on the surface of LCD panel	Stains which cannot be removed even when wiped lightly with a soft cloth or similar cleaning.	Minor								
9) Rainbow color	No rainbow color is allowed in the optimum contrast on state within the active area.	Minor								
10) Viewing area encroachment	Polarizer edge or line is visible in the opening viewing area due to polarizer shortness or sealing line.	Minor								
11) Bezel appearance	Rust and deep damages that are visible in the bezel are rejected.	Minor								
12) Defect of land surface contact	Evident crevices that are visible are rejected.	Minor								
13) Parts mounting	(1) Failure to mount parts (2) Parts not in the specifications are mounted (3) For example: Polarity is reversed, HSC or TCP falls off.	Minor								
14) Part alignment	(1) LSI, IC lead width is more than 50% beyond pad outline. (2) More than 50% of LSI, IC leads is off the pad outline.	Minor								
15) Conductive foreign matter (solder ball, solder hips)	(1) $0.45 < \Phi$ , $N \geq 1$ (2) $0.3 < \Phi \leq 0.45$ , $N \geq 1$ , $\Phi$ : Average diameter of solder ball (unit: mm) (3) $0.5 < L$ , $N \geq 1$ , L: Average length of solder chip (unit: mm)	Minor								
16) Bezel flaw	Bezel claw missing or not bent	Minor								
17) Indication on name plate (sampling indication label)	(1) Failure to stamp or label error, or not legible.(all acceptable if legible) (2) The separation is more than 1/3 for indication discoloration, in which the characters can be checked.	Minor								



## 6. Handling Precautions

### 6.1 Mounting method

A panel of LCD module made by our company consists of two thin glass plates with polarizers that easily get damaged. And since the module is so constructed as to be fixed by utilizing fitting holes in the printed circuit board (PCB), extreme care should be used when handling the LCD modules.

### 6.2 Cautions of LCD handling and cleaning

When cleaning the display surface, use soft cloth with solvent (recommended below) and wipe lightly.

- Isopropyl alcohol
- Ethyl alcohol
- Trichlorotrifluoroethane

Do not wipe the display surface with dry or hard materials that will damage the polarizer surface.

Do not use the following solvent:

- Water
- Ketene
- Aromatics

### 6.3 Caution against static charge

The LCD module uses C-MOS LSI drivers. So we recommend you:

Connect any unused input terminal to  $V_{dd}$  or  $V_{ss}$ . Do not input any signals before power is turned on, and ground your body, work/assembly areas, assembly equipment to protect against static electricity.

### 6.4 Packaging

-Module employs LCD elements, and must be treated as such. Avoid intense shock and falls from a height.

-To prevent modules from degradation, do not operate or store them exposed direct to sunshine or high temperature/humidity.

### 6.5 Caution for operation

-It is an indispensable condition to drive LCD module within the limits of the specified voltage since the higher voltage over the limits may cause the shorter life of LCD module.

-An electrochemical reaction due to DC (direct current) causes LCD undesirable deterioration so that the uses of DC (direct current) drive should be avoided.

-Response time will be extremely delayed at lower temperature than the operating temperature range and on the other hand at higher temperature LCD module may show dark color in them. However those phenomena do not mean malfunction or out of order of LCD module, which will come back in the specified operating temperature.

### 6.6 Storage

In the case of storing for a long period of time, the following ways are recommended:


- Storage in polyethylene bag with the opening sealed so as not to enter fresh air outside in it. And with not desiccant.
- Placing in a dark place where neither exposure to direct sunlight nor light is. Keeping the storage temperature range.
- Storing with no touch on polarizer surface by any thing else.

### 6.7 Safety

-It is recommendable to crash damaged or unnecessary LCD into pieces and to wash off liquid crystal by either of solvents such as acetone and ethanol, which should be burned up later.

-When any liquid leaked out of a damaged glass cell comes in contact with your hands, please wash it off well at once with soap and water.

## 7. Packaging Specifications

 <b>鑫洪泰</b> HOT DISPLAY		<b>Packaging Specifications</b>				Approved	Checked	Designed
		HTM12864Z						

### 7.1 Packaging Material

No	Item	Dimensions (mm)	IPCS Weight (KG)	Quantity	Total Weight
1	LCM	93.0*70.0*9.1	0.075	112	9.6
2	PE Bag	125*100	0.001	112	0.11
3	Foam Rubber Cushion	310*170	0.0175	8	1.8
4	Partition A1	310*170*100	0.30	4	1.2
5	Product Box	330*180*120 (neutral packing)	0.45	4	1.8
6	Carton	390*370*350 (neutral packing)	0.9	1	0.9
7	Tape			AR	
8	Label Specifications			1	
9	Label Rohs			1	
10	Label ESD			1	

7.2. Total LCD Weight in carton: 15.0 KG±10%

7.3. Packaging Specifications and Quantity:

(1) Quantity Of Spacer: A2\*6

(2) Total LCM quantity in carton: quantity per box 28\* no of boxes 4 = 112

